

Intel Ref.: P12891





## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: William R. Wheeler, et al.

Art Unit : 2825

Serial No.: 10/054,179

Filed

: January 17, 2002

Examiner: Annette M. Thompson Assignee : Intel Corporation

Title

: MODELING A LOGIC DESIGN

Exter W/ACE Olated 7-25-05 C. Starly 7-28-05

Mail Stop AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO FINAL OFFICE ACTION OF APRIL 22, 2005

Please amend the above-identified application as follows:

## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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